

PATENT NUMBER

3

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/941447	D	438	306	<del>220</del> 2812	Thomas

## APPLICANTS

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# TITLE

Semiconductor structures having multiple conductive layers in an opening, and methods for fabricating same

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12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b> The term of this patent subsequent to _____ (date) has been disclaimed. _____ (Assistant Examiner) _____ (Date)	<b>DRAWINGS</b> Sheets Drwg. Figs. Drwg. Print Fig.		<b>CLAIMS ALLOWED</b> Total Claims Print Claim for O.G.	
	<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ _____ (Primary Examiner) _____ (Date)		<b>NOTICE OF ALLOWANCE MAILED</b> _____ _____ _____ <b>ISSUE FEE</b> Amount Due _____ Date Paid _____ _____ _____	
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